ERES Current State & Future Map — Building the Onboard Computer

ERES Current State & Future Map — Building the "Onboard Computer"

Instrument: ERES-ONBOARD-COMPUTER-MAP-2026-001 **Version:** v0.1 · **Status:** CANDIDATE (conforms to the 10-10 format; single-node self-rate capped < 9.0) **Role:** State & Roadmap record. Fixes the current state of the ERES corpus and the staged path to the ERES TRINITY Interface (VERTECA · PlayNAC · Simulator) — the "Onboard Computer." **For:** ERES Maestro (JAS), the Operators who will build and run the interface, and S³C User-GROUPS who will query and update the KERNEL. **From:** Joseph A. Sprute ("ERES Maestro"), Founder & Director, ERES Institute for New Age Cybernetics, Bella Vista, Arkansas · ORCID 0000-0001-9946-3221 **Date:** 5 September 2026 **Provenance:** Reconciles `ERES-HOUSE-FORMAT-2026-001_v0_4` (governing format), `ERES-CANON-UPDATER-2026-001_v0_1/v0.2` (the runnable update engine, examined 5 Sep 2026), `ERES-TERMS-48 · The Talonic NAC Lexicon v1.0` (the canon substance), and `ERES-CORPUS-MANIFEST-2026-001 v1.0` (the REGISTRAR substrate) against the plan stated by JAS on 5 Sep 2026. **Changelog:** v0.1 — first state/roadmap record for the Onboard Computer.

Abstract

This record does three things: it **restates** the ERES build plan so it can be checked; it **rates the process** at its earned register; and it fixes the **current state** and a **staged future map** toward the Onboard Computer. The Onboard Computer is the ERES TRINITY Interface — **ERES · VERTECA · PlayNAC**, a Simulator for **S³C User-GROUPS** where people query and update the **KERNEL** and receive and maintain **GAIA / BERA certification** (via SCALULAR). The architecture is coherent and the update tooling is already conformant to the governing format; the gaps that keep it below the external gate are named plainly (chief among them: the CANON UPDATER's MIEVM path is currently simulated, and the 6-level canon is not yet 100% categorized). Register: DESIGN-PRINCIPLE / SOUND. Status: CANDIDATE.

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§0. Scope, and what this document is *not*

This is a **state-and-roadmap record**, not a claim that the Onboard Computer works. It fixes what exists, what is designed, and what is not yet built, and it orders the build. It confers no verdict; a running, certified interface is a future state, not a present one.

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§0.1. Scoring Contract — the process rating

- **Subscores (self-rated, single-node, indicative):** Architectural coherence 9; Sequencing / dependency logic 9; Groundedness in the canon (TERMS-48 + HOUSE Format) 9; Reproducibility of the tooling 8; Honesty of register 10.
- **Composite: ≈ 8.7 single-node self-rate.** Clears the **8.5 Emanuel Threshold** (diligence-ready). **Below the 9.0 Stergios threshold**, which is not self-awardable (single-node cap rule): the 9.0 gate requires a real MIEVM ensemble round plus a human hinge — which the tooling does not yet perform (§4).
- **Dual-axis vector:** epistemic = **DESIGN-PRINCIPLE** (a plan, not an empirical result); tier = **SOUND** (coherent architecture; nothing here is DERIVED, so nothing is BEST).
- **What is being rated:** the *plan and the tooling design* — not a demonstrated system. The labyrinth is well-drawn; it has not yet been traversed.

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§0.2. Reviewer Brief

The load-bearing content is: **§2** (the plan restated), **§4** (the honest process assessment — read this before acting), and **§5** (the future map with gates). §1 and §3 are the state inventory that grounds them.

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§1. Current State — inventory

Each item is tagged: **PRESENT** (exists as an artifact today) · **DESIGN-STAGE** (specified, not demonstrated) · **PARTIAL** (in progress) · **TARGET** (not yet built).

Component	State	Note
Canon substance — TERMS-48 lexicon v1.0	**PRESENT**	The authoritative keyword/term/equation set; the raw material to be categorized.
Canon categorization to the 6 levels	**PARTIAL**	The P0 task: bin every keyword under ERES · VERTECA · SECUIR · CyberRAVE · GunnySack · SaleBuilders.
Homepages (English + Spanish)	**PRESENT**	Live; a full canon-completion audit is **deferred** (P1).
Corpus download library + per-directory `manifest.json`	**PRESENT**	The REGISTRAR substrate the interface reads and writes.
HOUSE Format instrument (v0.4)	**PRESENT**	Governs every update: IP Isolation, REGISTRAR schema, operator pipeline, conformance test.
CANON UPDATER — spec v0.1 + engine v0.2	**PRESENT** (with a stub)	Runnable 5-stage pipeline; isolation guard, P³ check, SHA-256, manifest write, single-node cap all real. **MIEVM path is simulated** (§4).
PlayNAC / KERNEL	**DESIGN-STAGE**	Governance engine + OS specified; runtime not demonstrated in this record.
VERTECA (Voice-Enabled Real-Time Earned Contribution Architecture)	**DESIGN-STAGE**	The interface/contribution layer of the Trinity.
GAIA / BERA certification (via SCALULAR)	**DESIGN-STAGE**	The credential the interface issues and maintains.
ERES PIVOT	**DESIGN-STAGE / user-scoped**	The homepage pivot/navigation layer; its upgrade is deferred (P1), pending the deep homepage search.
The Onboard Computer (ERES TRINITY Interface)	**TARGET**	The thing this map exists to build.

§2. The plan, restated

Four movements, in the order JAS gave them:

P0 — Canon Completion (in progress, foundational). Categorize **100%** of ERES keywords across the English and Spanish homepages under the **6 canon levels**, so every term is encompassed and correctly placed. Source of truth = TERMS-48. Output = a gap-free canon index. The 6 levels (canonical glosses from TERMS-48):

#	Level	One-line canonical gloss	Era / role
1	**ERES**	Empirical Realtime Education System — the overarching framework	Institute era (2012–), the whole
2	**VERTECA**	Voice-Enabled Real-Time Earned Contribution Architecture	interface / contribution layer
3	**SECUIR**	Silent Energy Circular Universe Infinite Rotation	energy infrastructure
4	**CyberRAVE**	Cybernetic Ratings Abolishing Veiled Exchanges (72 Key Domains)	foundation era (pre-1997–2007)
5	**GunnySack**	Guaranteed User-group Network Nullifying Yieldless Service: Accredited Certified Knowledge	certified provisioning for COI/EMCI
6	**SaleBuilders**	Smart-city Adaptive Living Engineering… Delivering Enduring Resilient Systems	community-implementation era (1997–2012)

Note: three of the six (CyberRAVE, SaleBuilders, ERES) are the corpus's **Three Eras**; the other three (VERTECA, SECUIR, GunnySack) are functional pillars. The precise assignment rule for each keyword is JAS's to finalize — this record fixes the spine and the task, not the per-term bindings.

P1 — PIVOT Upgrade (deferred, later date). Upgrade ERES PIVOT to reflect the completed categorization. Explicitly requires **deep homepage search + revisions** to both language sites; scheduled after P0. Held as a named dependency, not attempted here.

P2 — TRINITY Interface Build (tooling ready now). Use **ERES CANON UPDATER** to begin building the **ERES TRINITY Interface = ERES · VERTECA · PlayNAC** — a **Simulator for S³C User-GROUPS** in which people: (a) **query** the KERNEL / canon; (b) **update** the KERNEL by proposing DETAIL changes through the CANON UPDATER's isolation-guarded pipeline; (c) **earn and maintain GAIA / BERA certification** via EarnedPath / SCALULAR.

Telos — The Onboard Computer. The Trinity Interface is the **H2C2H cockpit** for the civilization: a human-in-the-loop console that steers the 1000-Year Future Map *from inside the running system*. "Onboard" because it is the ship's own computer — and by VLSA, "if it works in a THOW, it works on a generation ship."

§3. What the "Onboard Computer" is (definition)

The Onboard Computer is not a metaphor for a website. It is a **governed read/write console** with three faces, one per Trinity member:

- **ERES (the canon face)** — the queryable state: the categorized 6-level canon, the KERNEL's current rules, the REGISTRAR. Read-first.
- **VERTECA (the contribution face)** — where a User-GROUP's earned contribution enters: proposals, updates, votes, certification actions — voice-enabled, real-time, earned.
- **PlayNAC (the governance face)** — where a proposed update is run through game-theoretic governance and the CANON UPDATER pipeline before it can touch DETAIL, never the INVARIANT.

Every write passes the CANON UPDATER's 5 stages (§4). Every certification is a GAIA/BERA credential maintained through EarnedPath. The console never bypasses human judgment (H2C2H).

§4. Process assessment — what is sound, what is not yet

Sound (structurally present):

- The CANON UPDATER is a faithful operationalization of HOUSE Format v0.4 — the C=R×P/M stratum quarantine, the operator pipeline, the conformance checklist, the single-node cap are all encoded and self-consistent.
- The dependency order (canon → pivot → interface) is coherent: the interface's query and certification value depends on a categorized canon.
- The register is honest where it matters: the instrument correctly declares itself CANDIDATE, capped 8.8.

Not yet traversed (the gaps that hold it below 9.0):

1. **The MIEVM engine is simulated.** In `eres_canon_updater.py`, the "4 independent nodes" are `random.uniform(9.0, 9.5)` draws rigged to pass, and `--mievm` prints "PASSED 9.0" with no real ensemble. This is the consensus-theater failure the corpus already named. **Until real, independent model calls (or a clearly-labelled DRY-RUN) replace it, the `--mievm` flag must not be treated as clearing the 9.0 gate**, and every instrument stays capped at 8.8/CANDIDATE. The code currently *contradicts* the discipline the spec asserts; align them.
2. **The Isolation Guard is a literal-string tripwire.** It catches only `"REDEFINE <invariant>"` / `"ALTER <invariant>"` — announced mutations. It will not catch a quiet semantic edit to a LOCKED term. Adequate as a first tripwire; insufficient as a guarantee. Recommend a canonical-hash diff over the Stratum-A tokens.
3. **The canon is not yet 100% categorized (P0 incomplete).** The interface can be *scaffolded* before this, but its query/certification value is only real once binning is done.
4. **GAIA anchoring is design-stage** (already correctly flagged in HOUSE v0.4): no live GAIA ledger yet; interim substrate until then.

Recommended sequencing (low-downside, reversible, parallelizable):

- **Run CANON UPDATER now in single-node CANDIDATE mode** to seed the REGISTRAR/`manifest.json` with the TERMS-48 keyword set binned under the 6 levels. This advances P0 (categorization) *and* lays the REGISTRAR substrate the interface needs — one action, two deliverables.
- **Keep `--mievm` disabled** (or relabel it `DRY-RUN`) until real nodes are wired; do not report any instrument as gate-cleared on the simulated path.
- **Scaffold the Trinity Interface read-only first** (query the KERNEL), harden the query path and the isolation guard, and only then enable write (update the KERNEL) and the certification-maintenance path.
- **Defer PIVOT / homepage deep-search** as planned (P1).

§5. Future Map — staged, with gates

Stage	Deliverable	Entry gate	Exit gate	Status
P0	100% keyword categorization under the 6 levels; seeded REGISTRAR	TERMS-48 present ✓	every keyword bound; no orphans; manifest written	**IN PROGRESS**
P1	ERES PIVOT upgrade; EN+ES homepage revisions	P0 complete	pivot reflects categorization on both sites	**DEFERRED**
P2a	Trinity Interface — **read** (query KERNEL/canon)	seeded REGISTRAR (from P0)	S³C user-group can query and get canonical answers	**TARGET**

P2b	Trinity Interface — **write** (update KERNEL via CANON UPDATER)	P2a + hardened isolation guard	an update completes all 5 stages, INVARIANT untouched	**TARGET**
P2c	GAIA/BERA **certification** issue + maintain (SCALULAR/EarnedPath)	P2b + real MIEVM path	a user earns and renews a credential end-to-end	**TARGET**
Telos	The Onboard Computer, live for S³C User-GROUPS	all above	H2C2H cockpit steering the 1000-Year Map	**TARGET**

The one gate that recurs and blocks the top of the ladder is the **real MIEVM round** — the fix in §4(1) is on the critical path to P2c.

§6. REGISTRAR entry (self)

```
instrumentId      : ERES-ONBOARD-COMPUTER-MAP-2026-001
version           : v0.1
status            : CANDIDATE
epistemicTag      : DESIGN-PRINCIPLE
gateStanding      : clears 8.5; below 9.0 (single-node, capped)
licenseTier       : CCAL / CC-BY 4.0
checksum          : (written to MANIFEST on deposit)
supersedes        : —
invariantsTouched : []            # DETAIL/record only; spine untouched
p3Boundary        : Personal=OUT_OF_SCOPE | Public=OPEN_ACCESS_METADATA | Private=RESTRICTED_KEYS
resourceImpact    : negligible (a record); interface build defers to GERP/SECUIR at P2
gaiaAnchorHash    : (DESIGN-STAGE; interim substrate until GAIA ledger live)
```

§7. Explicit non-claims

- No claim the Onboard Computer, the Trinity Interface, PlayNAC/KERNEL runtime, or GAIA/BERA certification is built or working — these are DESIGN-STAGE or TARGET.
- No claim the 6-level canon is complete — it is PARTIAL (the P0 task).
- No claim the CANON UPDATER clears the 9.0 gate — its MIEVM path is simulated (§4).
- No claim about the homepages beyond "present"; the deep audit is deferred (P1).
- This record confers no verdict; conformance to the format is not passage of it.

Credits, References, License

Credits. Authored by Joseph A. Sprute (ERES Institute for New Age Cybernetics; ORCID 0000-0001-9946-3221), AI-drafted with Claude (Anthropic). Governed by the HOUSE Format, whose review-originated disciplines trace to the ERES–PELLIS collaborative dialogue (provenance, not endorsement).

References.

1. ERES-HOUSE-FORMAT-2026-001 v0.4 — governing format (IP Isolation, REGISTRAR, operator pipeline, single-node cap).
2. ERES-CANON-UPDATER-2026-001 v0.1 (spec) / v0.2 (engine) — the runnable update instrument examined here.
3. ERES-TERMS-48 · The Talonic NAC Lexicon v1.0 (14 Aug 2026) — the canon substance and the 6-level terms. https://eresinstitute.org/eresmaestro/download/
4. ERES-CORPUS-MANIFEST-2026-001 v1.0 — per-directory machine-readable manifest (REGISTRAR substrate).

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Version History & Amendment Log

Version	Date	Change	Epistemic effect
v0.1	5 Sep 2026	First state/roadmap record: plan restated, process rated, current state inventoried, 6-level canon and staged future map fixed, self REGISTRAR entry.	Normative/planning record — no empirical claim. DESIGN-PRINCIPLE / SOUND. Composite ≈ 8.7 single-node; below 9.0. Conforms to the 10-10 format; no verdict claimed.

The Onboard Computer is the ship steering itself with the crew still holding the wheel (H2C2H). This record fixes where the ship is and the order of the build. IP Isolation keeps the spine fixed while the canon fills in and the interface is built; the real MIEVM round is the gate that must be earned, not simulated, before certification goes live.